Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2996

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 0.74Ω (typ.)

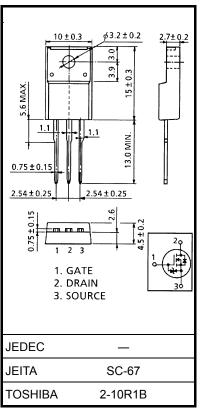
• High forward transfer admittance : $|Y_{fs}| = 6.8 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	10	Α	
	Pulse (Note 1)	I _{DP}	30	A	
Drain power dissipation	n (Tc = 25°C)	P_{D}	45	W	
Single pulse avalanche energy (Note 2)		E _{AS}	252	mJ	
Avalanche current		I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 4.41 mH, $R_G = 25 \Omega$, $I_{AR} = 10 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device.

Please handle with caution.



Electrical Characteristics (Ta = 25°C)

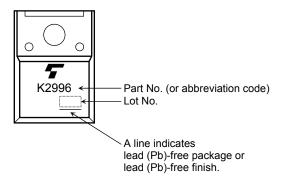
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	600	_	1	V
Gate threshold v	/oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A	_	0.74	1.0	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.4	6.8	1	S
Input capacitano	e	C _{iss}		_	1500	1	
Reverse transfe	transfer capacitance C_{rss} $V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		_	13	1	pF	
Output capacitance		Coss		_	140	1	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10 \text{ V}}{\text{O} \text{ V}} \stackrel{\text{I}_{D} = 5 \text{ A}}{\text{O} \text{ V}} \text{OUT}$ $R_{L} = 60 \Omega$ $V_{DD} = 300 \text{ V}$	_	15	-	
	Turn-on time	t _{on}		_	55	ı	ns
	Fall time	t _f		_	27	ı	115
	Turn-off time	t _{off}	Duty \leq 1%, t _w = 10 μ s	_	145	-	
Total gate charge (gate-source plus gate-drain)		Qg			38	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		21	_	nC
Gate-drain ("miller") charge		Q_{gd}			17	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

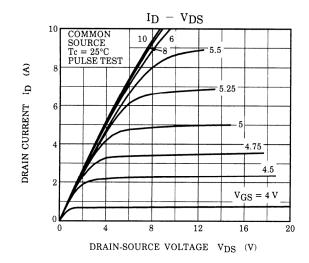
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V	ı	1600	1	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs		17	_	μC

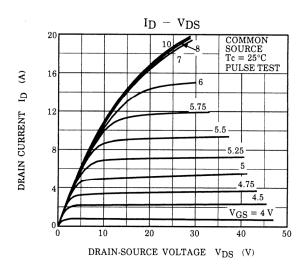
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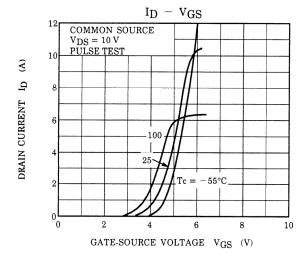
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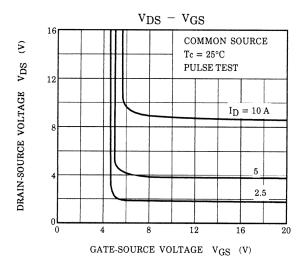


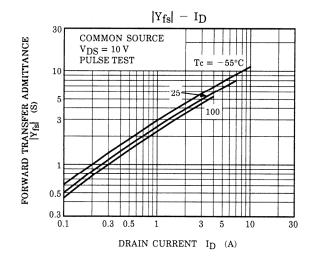
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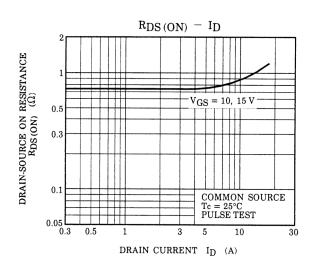


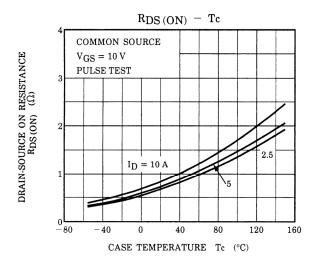


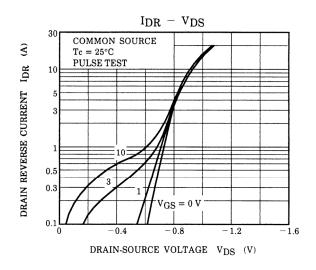


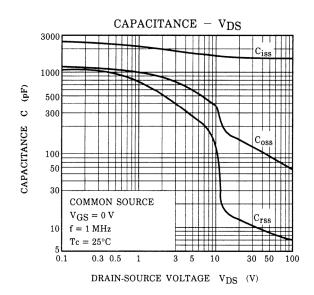


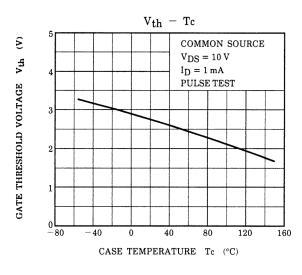


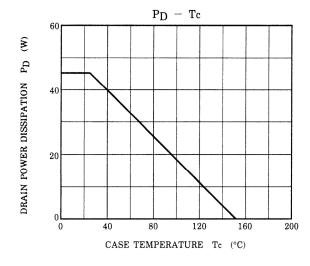


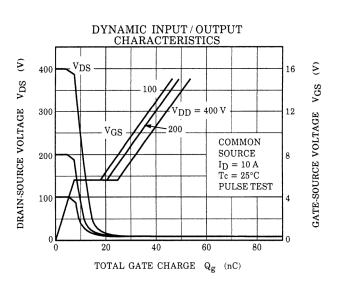


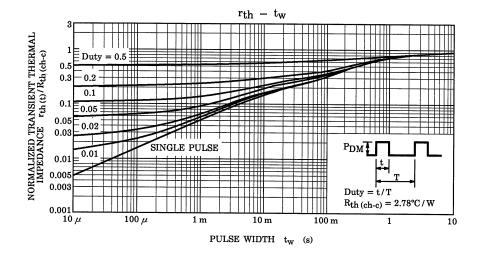


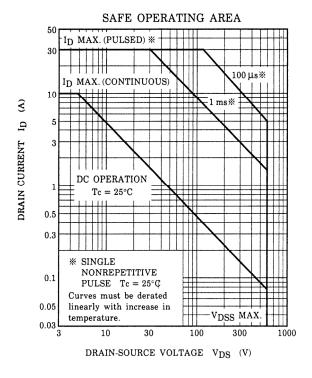


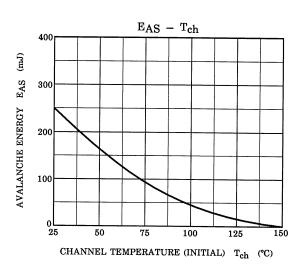


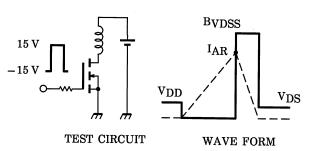












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 4.41~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}}\right) \end{aligned}$$

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